

Notice of References Cited	Application/Control No. 10/800,454	Applicant(s)/Patent Under Reexamination SWEET, JIM	
	Examiner Matthew D. Spittle	Art Unit 2111	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Camilleri, Nick. Status and Control Semaphore Registers Using Partial Reconfiguration. XILINX. June 7, 1999. Version 1.0
	V	Liu et al. Extending OpenMP for Heterogeneous Chip Multiprocessors. Institute for Scientific Computing, Wayne State University. Proceedings of the 2003 International Conference on Parallel Processing.
	W	Marshall, Joseph. Building Standard's Based COTS Multiprocessor Computer Systems for Space Around a High Speed Serial Bus Network. Lockheed Martin Federal Systems. 1998
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.